





SIMPLWINDOWS NAME: Biamp Tesira Command Processor RS232 v3.3 VERSION: 3.3 SUMMARY: This module controls all RS232 communication with the Biamp Tesira. This module is the core RS232 communication module for a suite of modules. The suite of modules utilizes the SIMPL# technology and will only work on the 3-Series Cornoller. Up to 32 instances of this module can be used in a single program slot. The processor is a suite of modules and the suite of modules are control modules and the suite of control modules. The control modules in this suite are control modules. The control modules are responsible for providing the cautal control modules. The control modules are responsible for providing the control modules are responsible for providing the command processor. The parameter that you assign to the instance of the command processor to be physically "connected" to the command processor. The your accessor to which they report to. You can virtually have an unlimited number of control modules report to a single instance of a command processor. A timf file (Demo.tmf) was created for Crestron testing purposes and MUST be used for proper operation of the Tesira Demo program. Once the processing module has determined that it is communicating with the Tesira, it will initialize any individual control modules that are registered to it. Once a control module receives all the responses it is looking for, it instruct the processing module that its initialization once all control modules are initialized that are registered with the processing module, the Is_initialized to the oral control modules are initialized that are registered control modules. If any components fall registration of the processing module, the Is_initialized tone all control includes are initialized that are registered to a quarantine pool until the configuration issue has been resolved. The number of quarantine proposents will propagate on the Quarantined, Count output on the processing module. In enumber of enabled and registered components will propa	GENERAL INFORMATION		
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GENERAL INFORMATION continued				
CRESTRON HARDWARE REQUIRED:	3-series & 4-series processors only (<i>Note: use 1.X modules for 2-series processors</i>)			
SETUP OF CRESTRON HARDWARE:	RS232 Baud: 115200 Parity: N Data Bits: 8 Stop Bits: 1			
VENDOR FIRMWARE:	Tesira Firmware - 4.2.4.5			







PARAMETER:		
Command_Processor_ID	Setting to indicate the ID for a particular processing module. Up to 32 separate processing modules may be used in a single program, each one operating independently. Note that if multiple processing modules are to be used in a single program, they <u>must</u> each have different ID's set.	
Username	Optional username field for authentication if Tesira Device/System security has been enabled.	
Password	Optional password field for authentication if Tesira Device/System security has been enabled.	







CONTROL:		
Initialize	D	Set this input high to auto-initialize all control modules (once the processing module establishes communication with the Tesira). Pulse to reinitialize all control modules.
Debug	D	Set this input high to allow internal trace messages to be printed in SIMPL Debugger. This is useful for debugging the processes going on inside the compiled SIMPL# code.
From_Device	S	Serial signal to be routed from a 2-way com port.







FEEDBACK:		
ls_Initialized	D	Set to high when all registered control modules have successfully indicated that they have received the required responses to all their queries.
Is_Communicating	D	Set to high when the processing module has successfully established communication with the Tesira and is receiving appropriate responses.
Enabled_Count	Α	This signal indicates the total number of enabled components that are registered with the command processor. Certain modules contain more than one component, as an example, the Basic LevelMute module. This module contains both a Level and a Mute component and will register both with the command processor.
Quarantined_Count	Α	This signal indicates the total number of components that are enabled and registered to the command processor, but failed initialization due to configuration issues. Certain modules contain more than one component, as an example, the Basic LevelMute module. This module contains both a Level and a Mute component and if the module defined contains a configuration issue, both components will be counted in the quarantine count.
To_Device	S	Serial signal to be routed to a 2-way com port.







TESTING:		
OPS USED FOR TESTING:	CP3: v 1.8001.0146	
SIMPL WINDOWS USED FOR TESTING:	4.2000.00.00	
CRES DB USED FOR TESTING:	211.05.001.00	
DEVICE DATABASE:	200.175.001.00	
SYMBOL LIBRARY USED FOR TESTING:	1164	
SAMPLE PROGRAM:	Biamp Tesira v3.3 RS232 Demo	
REVISION HISTORY:	v2.0 – Initial Release v2.1 – Updated to support TesiraLUX. Added 30 more command processor ID to the selection list. v2.2 – No revisions have been performed. v2.3 – No revisions have been performed. v3.0 - Added SIMPL# event callbacks. Reworked module Registration. Fixed issues with RS232 not reestablishing communication after loss of comm. Changed around how the subscription watchdog logic works. Once initialization is achieved, the subscription watchdog begins a patrolling or revolving method that tests each subscription capable registered instance tag. v3.1 – Added quarantine functionality. v3.2 – Fixed subscription registration issue v3.3 – No revisions have been performed.	